

ABSTRACT OF THE DISCLOSURE

A non-volatile semiconductor memory device usable in relatively wide applications and a method of manufacturing the same are provided. A NOR-type flash
5 memory region (2) including a NOR-type memory cell transistor and a DINOR-type flash memory region (3) including a DINOR-type memory cell transistor are manufactured into a single semiconductor chip (1). A peripheral circuit region (7) including a transistor for a peripheral circuit or the like is manufactured into a region surrounding the NOR-type flash memory region (2) and the DINOR-type flash memory region (3). The peripheral
10 circuit region (7) is shareable between the NOR-type flash memory region (2) and the DINOR-type flash memory region (3) by electrical connection to both of the regions.